

PALM INTRANET

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Inventor Name Search Result

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Last Name = SCHENK First Name = HEINRICH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06236823	4435773	150	02/20/1981	RECEIVER FOR EFFECTING SYNCHRONOUS DEMODULATION	SCHENK, HEINRICH
06677361	4688245	150	12/03/1984	METHOD AND CIRCUIT ARRANGEMENT FOR COMPENSATING CROSS- TALK AND/OR ECHO SIGNALS	SCHENK, HEINRICH
06681596	4701873	150	12/14/1984	METHOD AND A CIRCUIT ARRANGEMENT FOR DIGITAL SIGNAL PROCESSING UTILIZING ADAPTIVE TRANSVERSAL FILTER TECHNIQUES	SCHENK, HEINRICH
07156504	4937813	150	02/16/1988	SYSTEM FOR CANCELLING ECHO SIGNALS	SCHENK, HEINRICH
07250883	4933891	150	09/29/1988	METHOD AND CIRCUIT CONFIGURATION FOR GENERATING FILTER COEFFICIENTS	SCHENK, HEINRICH
07400192	5065412	150		PROCESS AND CIRCUIT ARRANGEMENT FOR DIGITAL CONTROL OF THE PHASE OF SCANNING CLOCK PULSES	SCHENK, HEINRICH
07400293	5230012	150	08/29/1989	PROCESS AND CIRCUIT ARRANGEMENT FOR DIGITAL CONTROL OF THE FREQUENCY AND/OR PHASE OF SCANNING CLOCK PULSES	SCHENK, HEINRICH
07412744	5088109	150		CIRCUIT ARRANGEMENT FOR THE EQUALIZATION OF DIGITAL SIGNALS RECEIVED	SCHENK, HEINRICH

				IN ANALOG FORM	
07412760	5027370	150	<u> </u>	CIRCUIT ARRANGEMENT	SCHENK, HEINRICH
07615162	5091703	150	11/19/1990	ANALOG LINE CONNECTION	SCHENK, HEINRICH
07656222	Not Issued	161	02/15/1991	ARRANGEMENT FOR GENERATING A COMPENSATION SIGNAL FOR DIGITAL TRANSMISSION EQUIPMENT	SCHENK, HEINRICH
07656236	Not Issued	166		METHOD AND CIRCUIT ARRANGEMENT FOR LEVEL MONITORING	SCHENK, HEINRICH
08145671	5323331	150	11/04/1993	METHOD AND CIRCUIT ARRANGEMENT FOR LEVEL MONITORING	SCHENK, HEINRICH
08387188	5572520	150	02/13/1995	METHOD AND CIRCUIT ARRANGEMENT FOR CONTROLLING THE TRANSMISSION OF MESSAGE BLOCKS WITHIN A TRANSMISSION SYSTEM	SCHENK, HEINRICH
09326365	6381623	150	06/04/1999	METHOD FOR ADAPTIVE FILTER ADJUSTMENT IN A QAM/CAP SYSTEM	SCHENK, HEINRICH
09408475	6731611	150	09/28/1999	METHOD FOR DUPLEX DATA TRANSMISSION WITH QAM AND DEMODULATOR FOR USE IN THE METHOD	SCHENK, HEINRICH
09433384	6529925	150	11/03/1999	METHOD FOR REDUCING THE CREST FACTOR OF A SIGNAL	SCHENK, HEINRICH
09449717	6647076	150	11/24/1999	METHOD OF COMPENSATING FOR INTERFERENCE IN A SIGNAL GENERATED BY DISCRETE MULTITONE MODULATION, AND CIRCUIT CONFIGURATION FOR CARRYING OUT THE METHOD.	SCHENK, HEINRICH
09468375	6396802	150	11	DATA TRANSMISSION METHOD WITH A PLURALITY OF FREQUENCY BANDS	SCHENK, HEINRICH
09599279	6563870	150	06/22/2000	NONLINEAR ECHO	SCHENK,

				COMPENSATOR	HEINRICH
09627732	6466631	150	07/28/2000	DEVICE AND METHOD FOR CHANGING THE NOISE CHARACTERISTIC IN A RECEIVER OF A DATA TRANSMISSION SYSTEM	SCHENK, HEINRICH
09729064	6370206	150	12/04/2000	Adaptive CAP receiver and method for controlling a cap receiver	SCHENK, HEINRICH
09806140	6987812	150	06/27/2001	DIGITAL RECEIVER FOR A SIGNAL GENERATED WITH DISCRETE MULTI-TONE MODULATION	SCHENK, HEINRICH
09815652	6714613	150	II I	APPARATUS AND METHOD FOR CONTROLLING THE SAMPLING CLOCK IN A DATA TRANSMISSION SYSTEM	SCHENK, HEINRICH
09914635	7031410	150	02/25/2002	METHOD OF NOISE CANCELLATION IN A SIGNAL GENERATED BY DISCRETE MULTI-TONE MODULATION AND CIRCUIT FOR CARRYING OUT SAID METHOD	SCHENK, HEINRICH
09951240	6608532	150	09/12/2001	CIRCUIT CONFIGURATION FOR PRODUCING A QUADRATURE-AMPLITUDE- MODULATED TRANSMISSION SIGNAL	SCHENK, HEINRICH
10049252	Not Issued	71	06/03/2002	Reception method and receiver array for a duplex transmission system	SCHENK, HEINRICH
10129370	Not Issued	90	09/23/2002	ECHO COMPENSATION DEVICE	SCHENK, HEINRICH
10150342	7095800	150	05/17/2002	DIGITAL PRECODING FILTER FOR A TRANSMISSION FILTER	SCHENK, HEINRICH
10157742	7042953			METHOD AND ARRANGEMENT FOR COMPENSATING SIGNAL ECHOES DURING DUPLEX DATA TRANSMISSION WITH DISCRETE MULTITONE MODULATION	SCHENK, HEINRICH
10203439	6831578	150	10/21/2002	METHOD AND CIRCUIT	SCHENK,

				ARRANGEMENT FOR DEMODULATION OF A QUADRATURE AMPLITUDE- OR PHASE-MODULATED SIGNAL	HEINRICH
10296118	Not Issued	71	04/01/2003	Method and device for controlling the timing of a digital receiver	SCHENK, HEINRICH
10351900	7113561	150	01/27/2003	CLOCK CONTROL CIRCUIT FOR CONTROLLING THE CLOCK PHASE OF A TRANSCEIVER	SCHENK, HEINRICH
10390831	Not Issued	77	03/18/2003	Calculation circuit for calculating a sampling phase error	SCHENK, HEINRICH
10466502	7161981	150	11/04/2003	DECISION FEEDBACK EQUALIZING DEVICE	SCHENK, HEINRICH
10477635	Not Issued	71	12/29/2003	Method and system for transferring data with Tomlinson-coding	SCHENK, HEINRICH
10485064	7050487	150	10/07/2004	METHOD AND CIRCUIT ARRANGEMENT FOR DETERMINATION OF TRANSMISSION PARAMETERS	SCHENK, HEINRICH
10493345	Not Issued	41	04/22/2004	Splitter circuit	SCHENK, HEINRICH
10629310	6930567	150	07/28/2003	TRANSMISSION SYSTEM	SCHENK, HEINRICH
10656383	Not Issued	41	09/05/2003	Circuit arrangement and method for compensating for disturbances in a signal generated by means of discrete multitone modulation	SCHENK, HEINRICH
10763045	Not Issued	30	01/21/2004	Method and device for reducing the crest factor of a signal	SCHENK, HEINRICH
10763046	Not Issued	30	01/21/2004	Method and device for reducing the crest factor of a signal	SCHENK, HEINRICH
10766338	Not Issued	30	01/28/2004	Method and switching arrangement for the identification of pupin coils	SCHENK, HEINRICH
10799353	Not Issued	30	03/12/2004	Method and apparatus for reducing the crest factor of a signal	SCHENK, HEINRICH
10840946	Not Issued	30	05/07/2004	Method and circuit for crest factor reduction	SCHENK, HEINRICH
11042571	Not Issued	30	01/25/2005	Fractional spaced equalizer	SCHENK, HEINRICH

11073429	Not Issued	30			SCHENK, HEINRICH
11241833	Not Issued	30		Method and receiver circuit for reducing RFI interference	SCHENK, HEINRICH
11241856	Not Issued	30		Method and circuit arrangement for reducing RFI interference	SCHENK, HEINRICH
11270312	Not Issued	30		Method and device for reducing the crest factor of a signal	SCHENK, HEINRICH

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